

S. J
#4 2-502
Pre and H/H

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Atty. Docket

ERIC J. VAN DER ZWAN ET AL NL 000364

Serial No.

Filed: CONCURRENTLY

A/D CONVERTER WITH INTEGRATED BIASING FOR A MICROPHONE

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows:

IN THE CLAIMS

[Please amend the claims as follows:]

4. (Amended) A combination as claimed in claim 2, characterized in that the gain of the second feed-back loop, which comprises the loop filter, the sampler and the second feed-back circuit, is several orders of magnitude higher than unity.

5. (Amended) A combination as claimed in claim 3, characterized in that the low pass filter is a digital filter and is included in the second feed-back circuit before the second digital-to-analog converter.